| | 1 | 41. | The method of claim 19, wherein said transferring said message comprises: |
|---|---|----------------|--|
| | 2 | | passing said message between said first task and said second task by |
| | 3 | | performing a fast-path message copy if said thread is queued to said |
| | 4 | | thread queue; and |
| | 5 | | passing said message between said first task and said second task by |
| | 6 | | performing a message copy if said thread is not queued to said thread |
| | 7 | | queue. |
| | 1 | 42. | The method of claim 4, wherein said performing said fast-path message copy |
| | 2 | comprises: | |
| | 3 | • | copying said message from a memory space of said first task to a memory |
| Ω 1 | 4 | | space of said second task. |
| Contd | 1 | 43. | The method of claim 41, wherein said performing said message copy |
| (Thta. | 2 | comprises: | The same of the sa |
| | 3 | F | copying said message from said first task to said thread control block/message |
| | 4 | • | structure; |
| | 5 | | waiting for said thread to be queued to said thread queue; and |
| | 6 | | copying said message from said thread control block/message structure to said |
| | 7 | | second task. |
| | 1 | 44. | The method of claim 41, wherein said first task acts as a client task and said |
| | 2 | second task ac | ets as a server task. |
| | 1 | 45. | A computer program product encoded in computer readable media, said |
| | 2 | computer prog | gram product comprising: |
| | 3 | a first | set of instructions, executable on a computer system, configured to send a |
| I AM Obstance | 4 | | message between a first task and a second task by performing a send operation, |
| LAW OFFICES OF SKJERVEN MORRILI MACPHERSON LLP | 5 | | wherein said first task performs said send operation and said send operation |
| 25 METRO DRIVE SUITE 700 SAN JOSE, CA 95110 (408) 453-9200 FAX (408) 453-7979 | 6 | | employs a thread control block/message structure; |

| / | a seç | ond soil instructions, executable on said computer system, configured to cause | | | | |
|----------------|-----------------|--|--|--|--|--|
| 8 | | said second task to perform a receive operation. | | | | |
| | | | | | | |
| 1 | 46. | The computer program product of claim 45, wherein said thread control | | | | |
| 2 | block/messag | ge structure comprises: | | | | |
| 3 | a thre | ead control block, wherein said thread control block is described by a first data | | | | |
| 4 | | structure, and | | | | |
| 5 | a mes | ssage, wherein said message is described by a second data structure and said first | | | | |
| 6 | | data structure comprises said second data structure. | | | | |
| 1 | 47. | The computer program product of claim 45, wherein said thread control | | | | |
| 2 | block/messag | ge structure supports control of a thread within said second task and said | | | | |
| 3 | computer pro | ogram product further comprises: | | | | |
| 4 | a thir | d set of instructions, executable on said computer system, configured to | | | | |
| 5 | | determine if said thread is queued to a thread queue of said second task; and | | | | |
| 6 | a four | rth set of instructions, executable on said computer system, configured to transfe | | | | |
| 7 | | said message from said first task and said second task. | | | | |
| 1 | 48. | The computer program product of claim 47, wherein said fourth set of | | | | |
| 2 | instructions of | instructions comprises: | | | | |
| 3 | a first | subset of instructions, executable on said computer system, configured to pass | | | | |
| 4 | | said message between said first task and said second task by performing a fast- | | | | |
| 5 | | path message copy if said thread is queued to said thread queue; and | | | | |
| 6. | a seco | ond subset of instructions, executable on said computer system, configured to | | | | |
| 7 | | pass said message between said first task and said second task by performing a | | | | |
| 8 | | message copy if said thread is not queued to said thread queue. | | | | |
| 1 | 49. | The computer program product of claim 48, wherein said first subset of | | | | |
| 2 | instructions of | comprises: | | | | |
| ¹ 3 | a first | sub-subset of instructions, executable on said computer system, configured to | | | | |
| 4 | | copy said message from a memory space of said first task to a memory space | | | | |
| 5 | | of said second task. | | | | |

| 1 | 50. The computer program product of claim 48, wherein said second subset of | | |
|---|--|--|--|
| 2 | instructions comprises: | | |
| 3 | a first sub-subset of instructions, executable on said computer system, configured to | | |
| 4 | copy said message from said first task to said thread control block/message | | |
| 5 | structure;\ | | |
| 6 | a second sub-subset of instructions, executable on said computer system, configured to | | |
| 7 | wait for said thread to be queued to said thread queue; and | | |
| 8 | a third sub-subset of instructions, executable on said computer system, configured to | | |
| 9 | copy said message from said thread control block/message structure to said | | |
| 10 | second task. | | |
| • | | | |
| $\bigcap_{i \in I} \int_{I} dI$ | 51. The computer program product of claim 48, wherein said first task acts as a | | |
| amt d.2 | client task and said second task acts as a server task. | | |
| Co | | | |
| 1 | 52. A computer system comprising: | | |
| 2 | a processor; | | |
| 3 | computer readable medium coupled to said processor; and | | |
| 4 | computer code, encoded in said computer readable medium, configured to cause said | | |
| 5 | processor to: | | |
| 6 | send a message between a first task and a second task by performing a send | | |
| 7 | operation, wherein said first task performs said send operation and said | | |
| 8 | send operation employs a thread control block/message structure; and | | |
| 9 | cause said second task to perform a receive operation. | | |
| | | | |
| 1 | 53. The computer system of claim 52, wherein said thread control block/message | | |
| 2 | structure comprises: | | |
| 3 | a thread control block, wherein said thread control block is described by a first data | | |
| LAW OFFICES OF | structure, and | | |
| SKJERVEN MORRILL MACPHERSON LL® 5 25 METRO DRIVE | a message, wherein said message is described by a second data structure and said first | | |
| SUITE 700 SAN JOSE, CA 95110 6 (408) 453-9200 FAX (408) 453-7979 | data structure comprises said second data structure | | |

| 1 | 54. The computer system of claim 52, wherein said thread control block/message |
|------------------|--|
| 2 | structure supports control of a thread within said second task and said computer code is |
| 3 | further configured to cause said processor to: |
| 4 | determine if said thread is queued to a thread queue of said second task; and |
| 5 | transfer said message from said first task and said second task. |
| | |
| 1 | 55. The computer system of claim 54, wherein said computer code further |
| 2 | configured to cause said processor to transfer said message from said first task and said |
| 3 | second task is further configured to cause said processor to: |
| 4 | pass said message between said first task and said second task by performing a fast- |
| 5 | path message copy if said thread is queued to said thread queue; and |
| 6 | pass said message between said first task and said second task by performing a |
| .7 | message copy if said thread is not queued to said thread queue. |
| | (X, Y, |
| 1 | 56. The computer system of claim 55, wherein said computer code further |
| 2 | configured to pass said message between said first task and said second task by performing a |
| 3 | fast-path message copy is further configured to cause said processor to: |
| 4 | copy said message from a memory space of said first task to a memory space of said |
| 5 | second task. |
| | |
| 1 | 57. The computer system of claim\55, wherein said computer code further |
| 2 | configured to pass said message between said first task and said second task by performing a |
| 3 | message copy is further configured to cause said processor to: |
| 4 | copy said message from said first task to said thread control block/message structure; |
| 5 | wait for said thread to be queued to said thread queue; and |
| 6 | copy said message from said thread control block/message structure to said second |
| 7 | task. |
| 7 | |
| L ^L 1 | 58 The computer system of claim 55, wherein said first task acts as a client task |

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and said second task acts as a server task.

CONCLUSION

In view of the amendments and remarks set forth herein, the application is believed to be in condition for allowance and a notice to that effect is solicited. Nonetheless, should any issues remain that might be subject to resolution through a telephonic interview, the Examiner is invited to telephone the undersigned.

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